

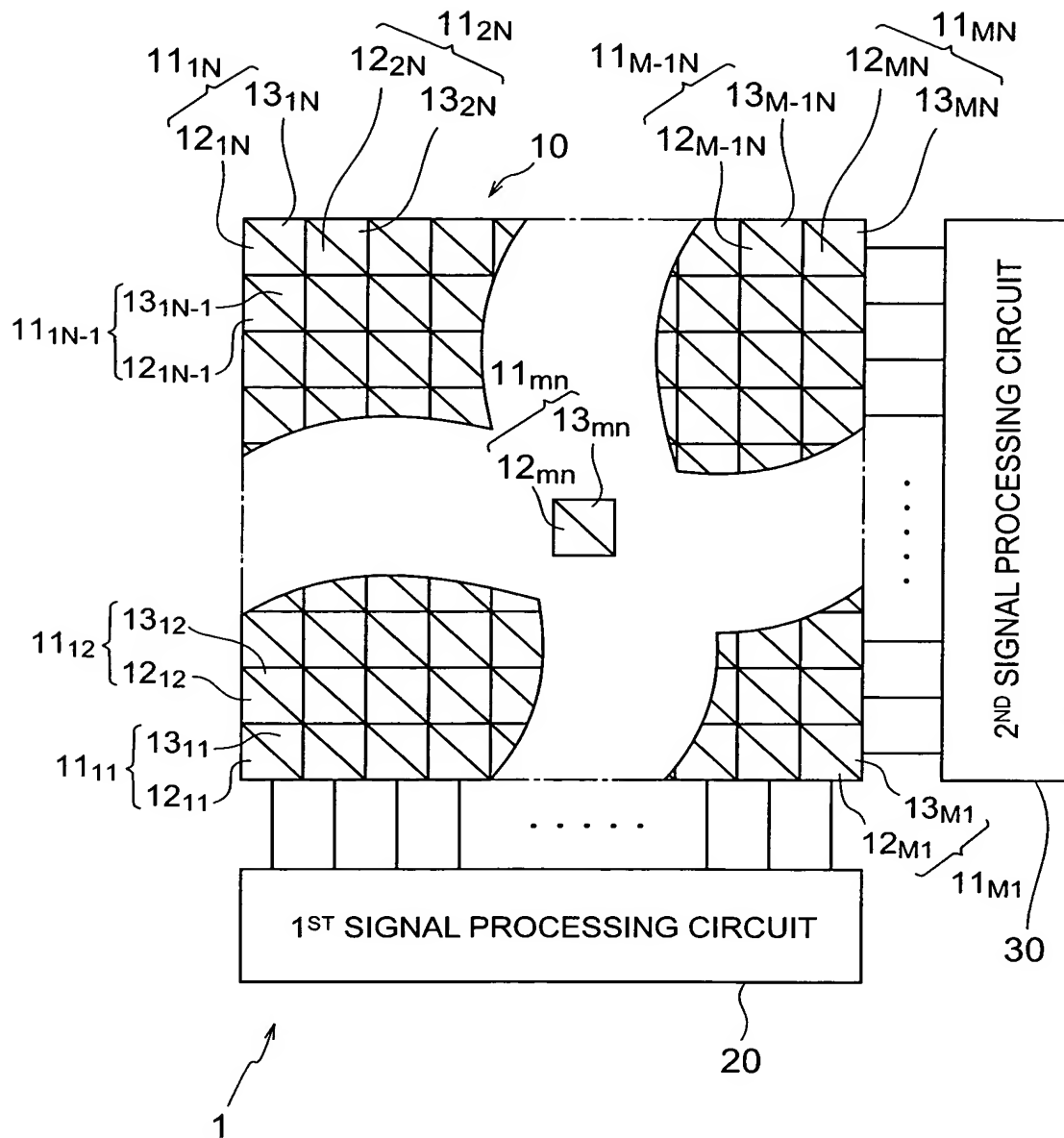
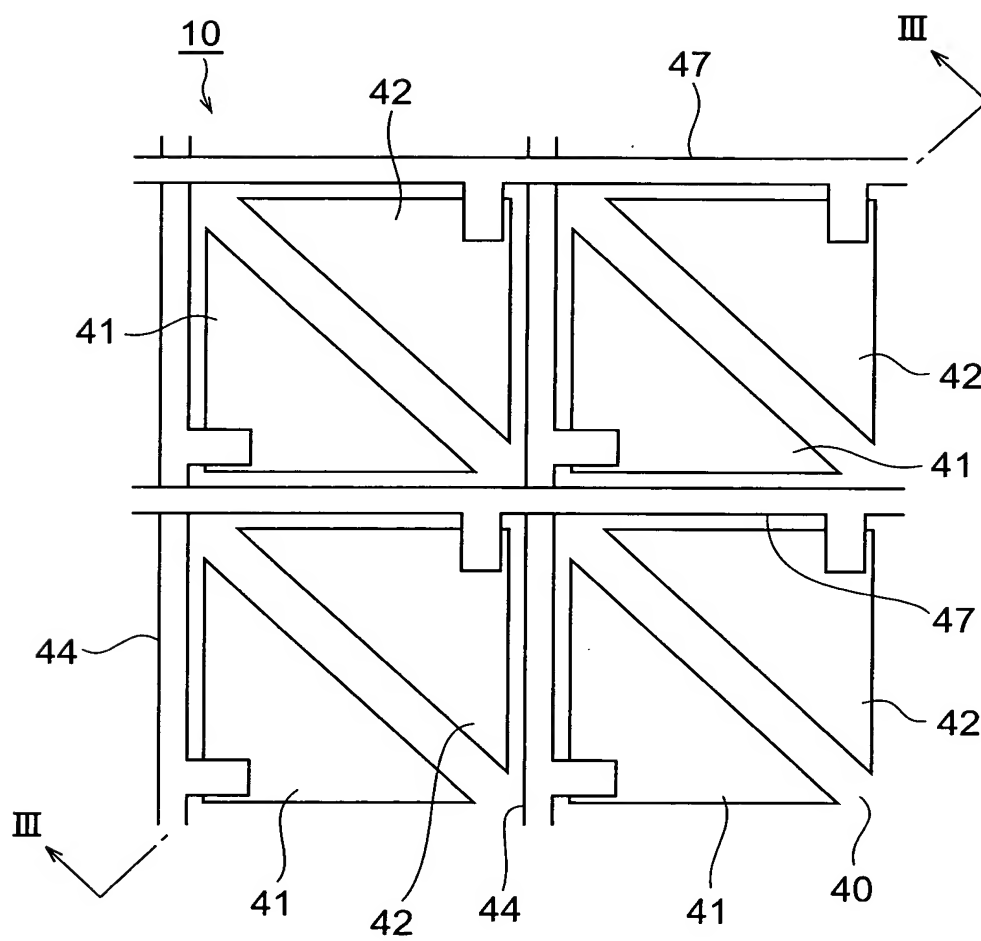
Fig. 1

Fig.2

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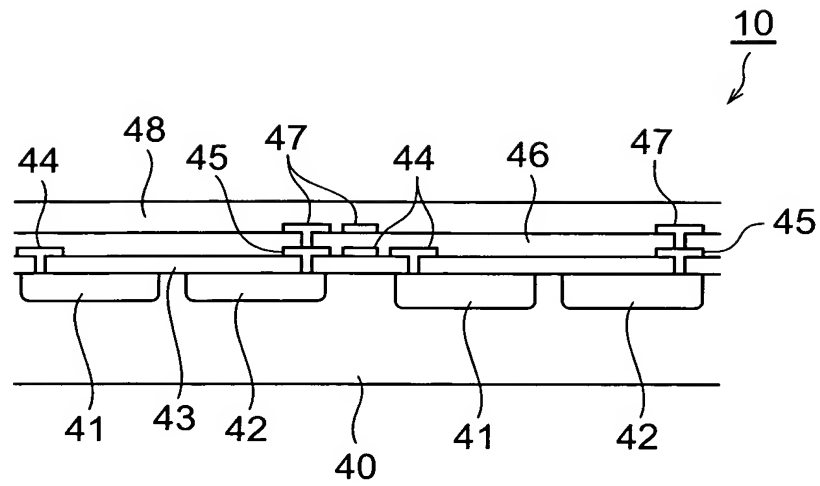
Fig.3

Fig.4

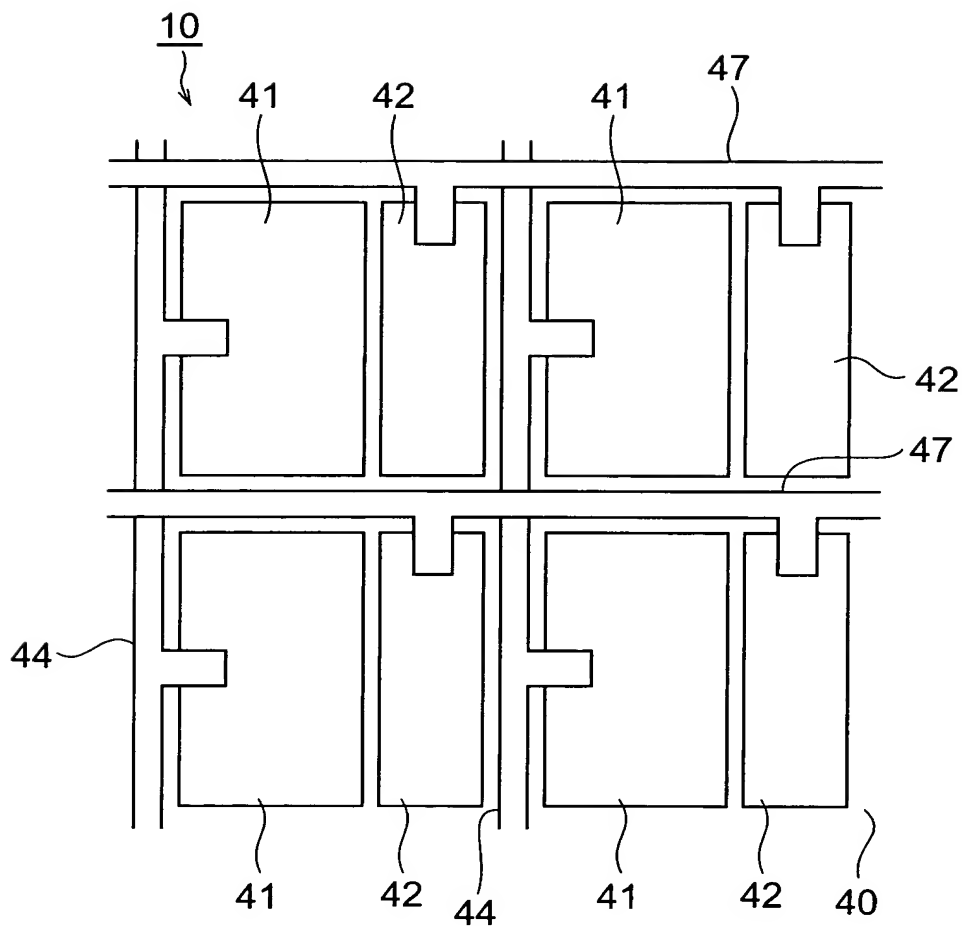


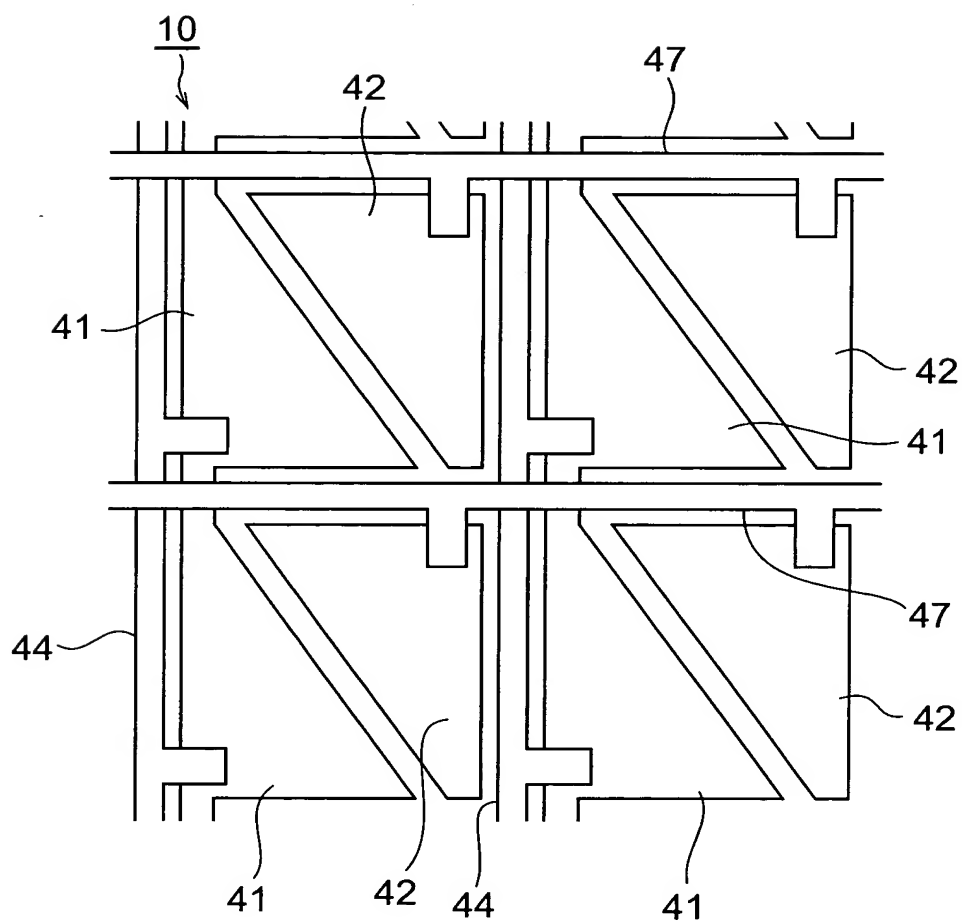
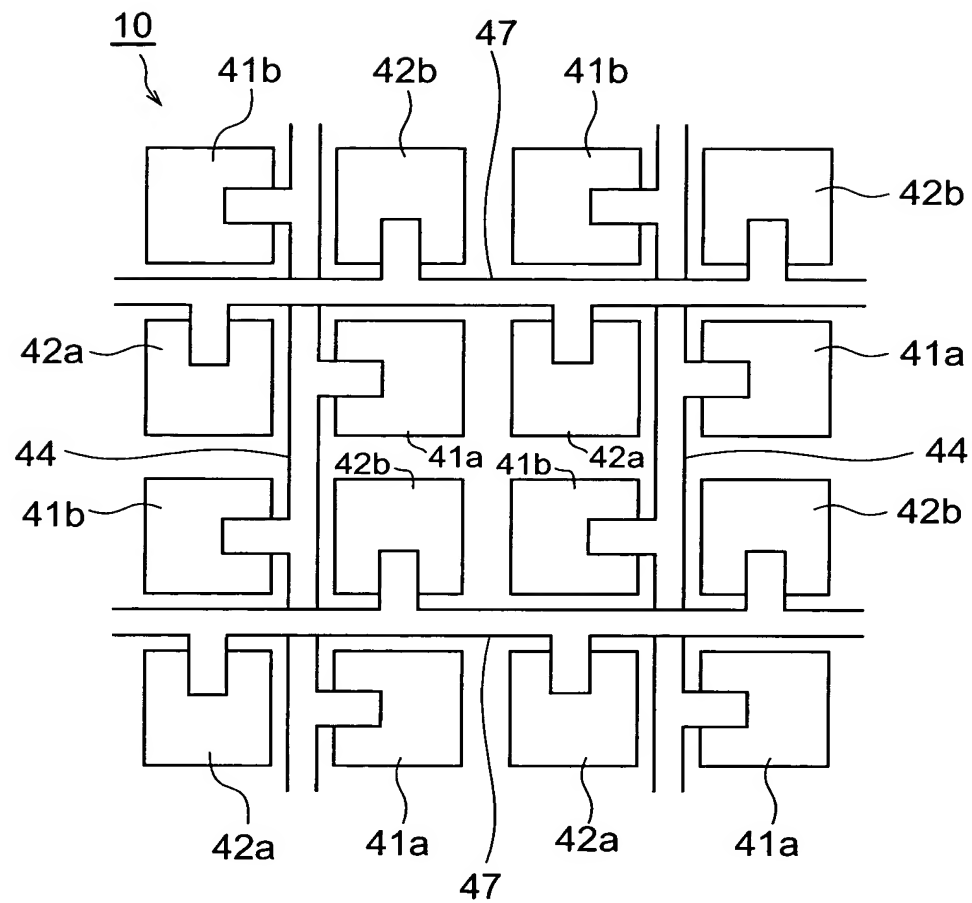
Fig.5

Fig.6

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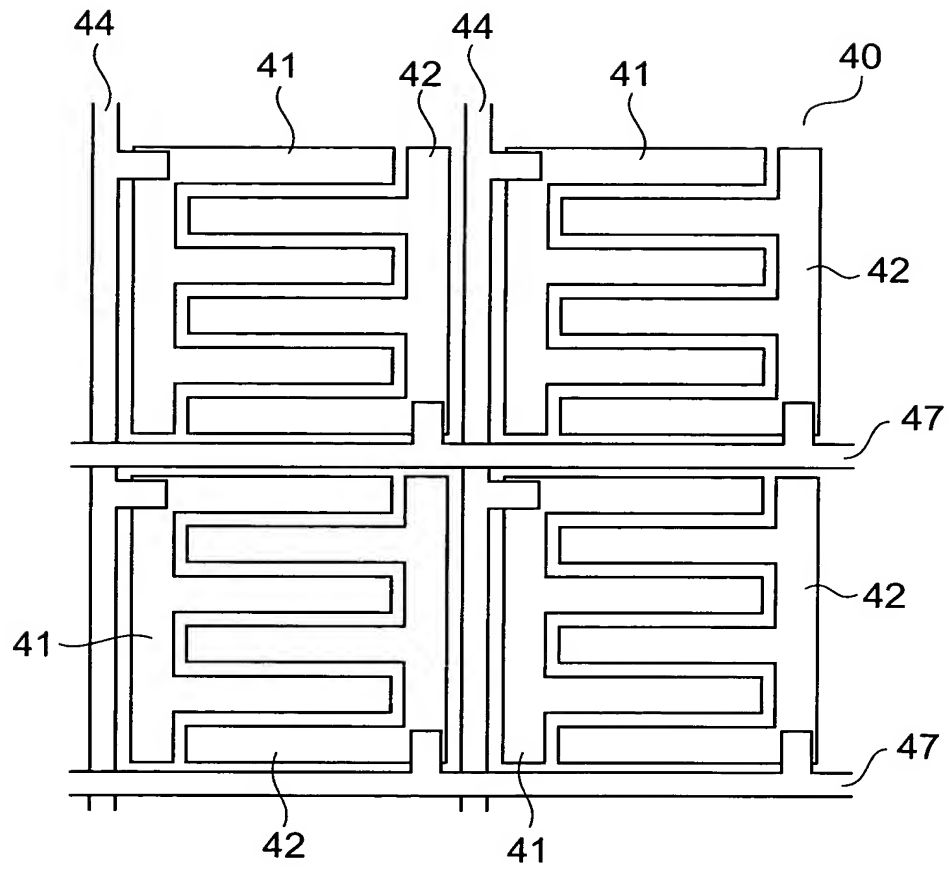
Fig.7

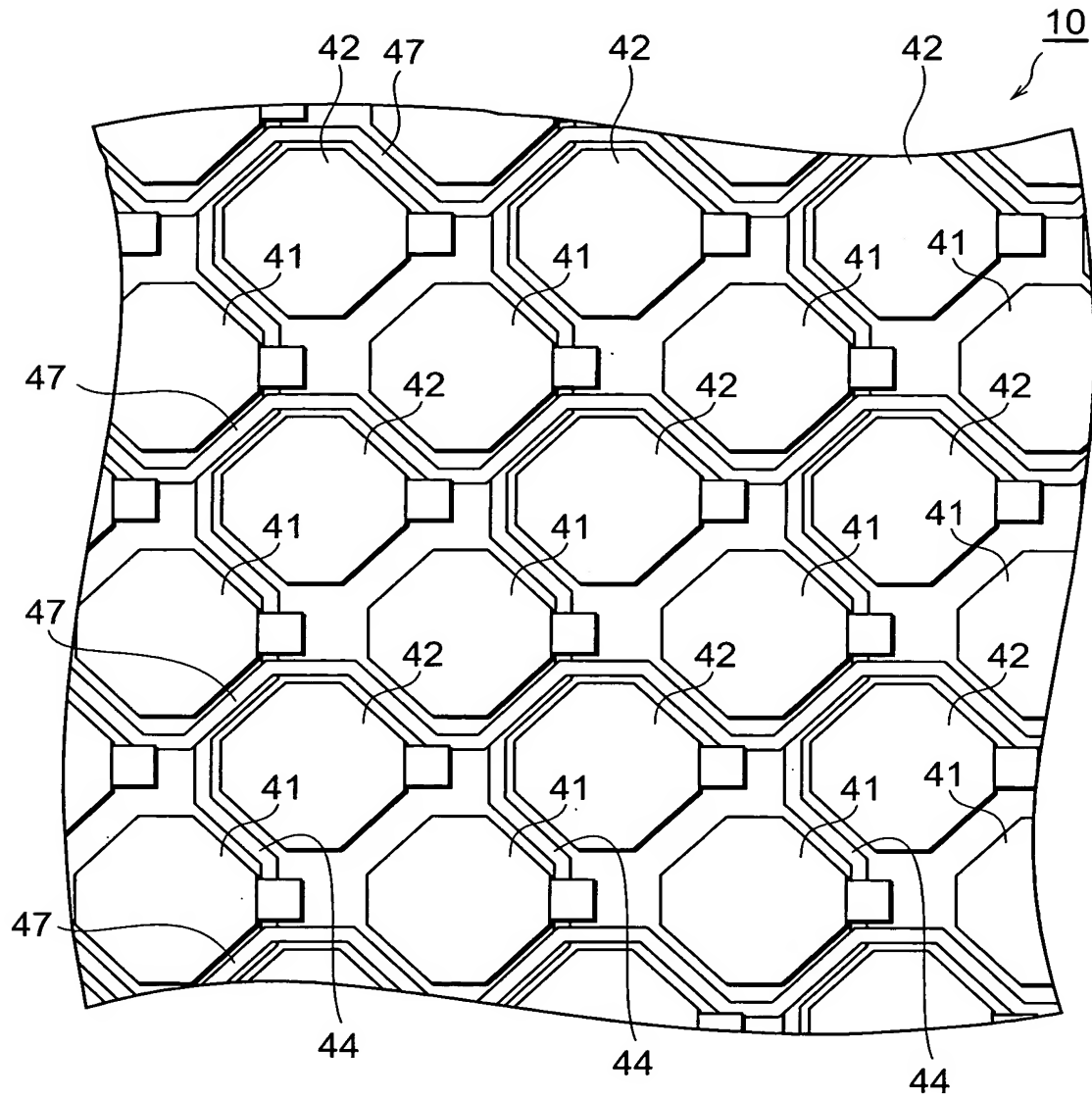
Fig.8

Fig.9

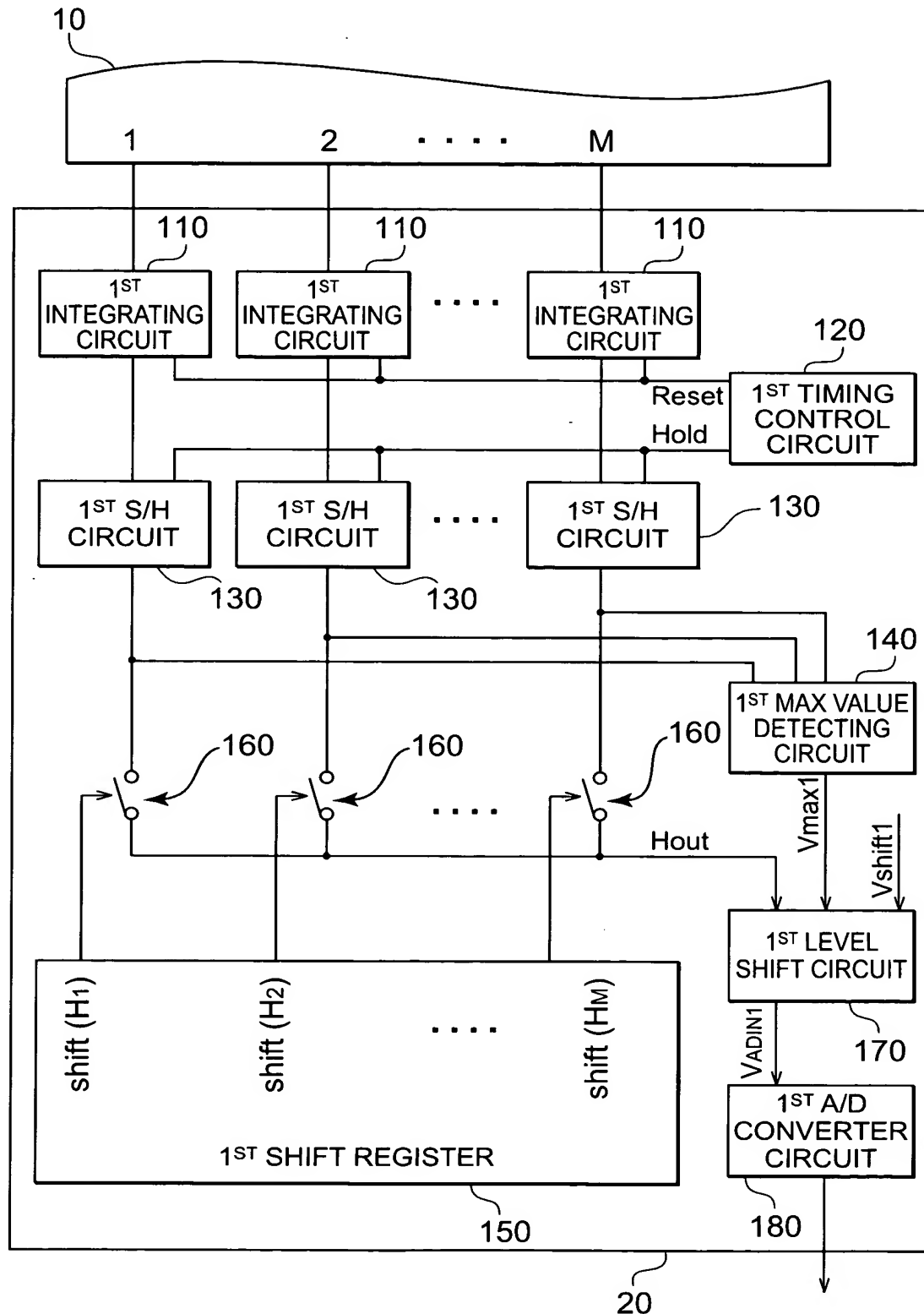
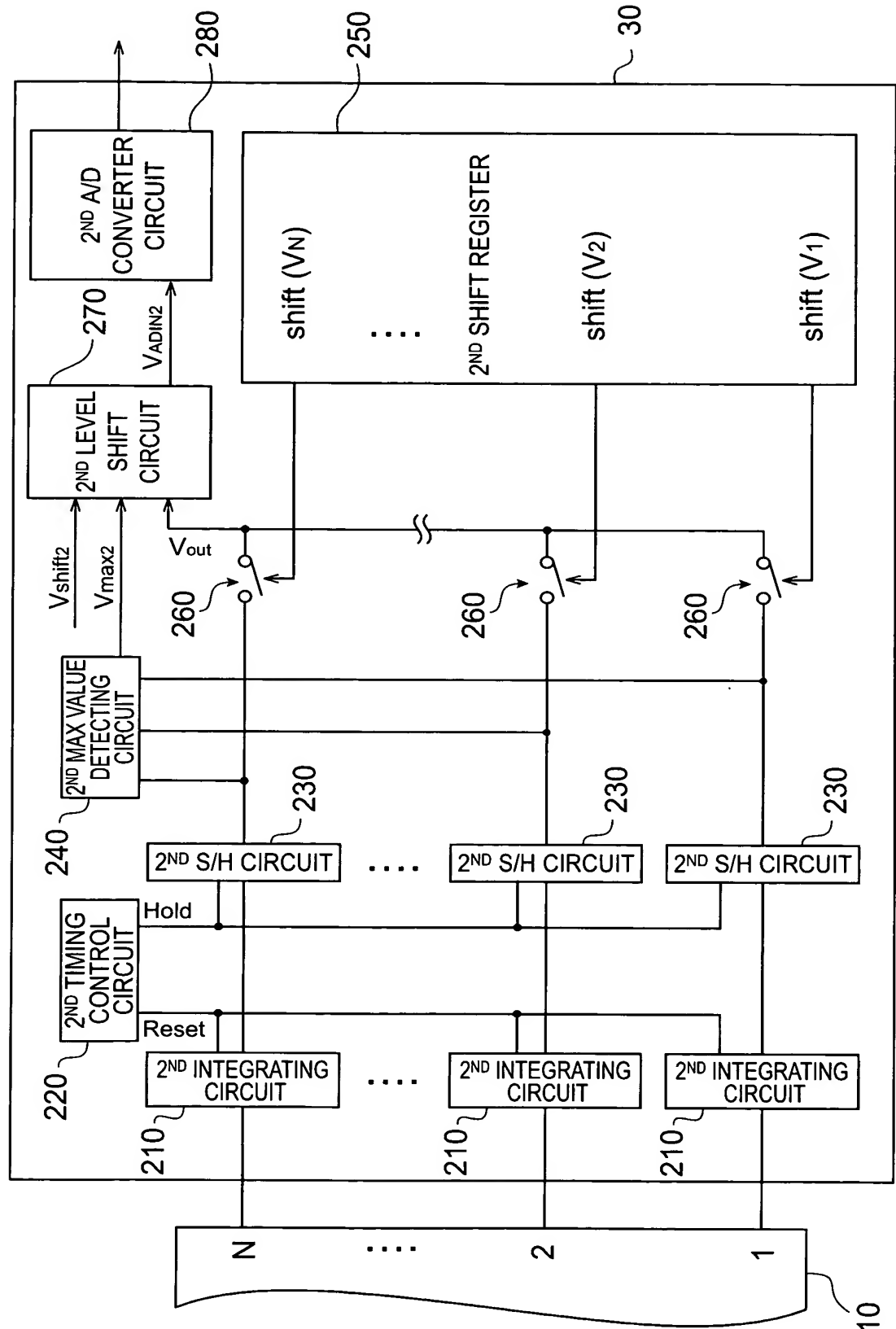
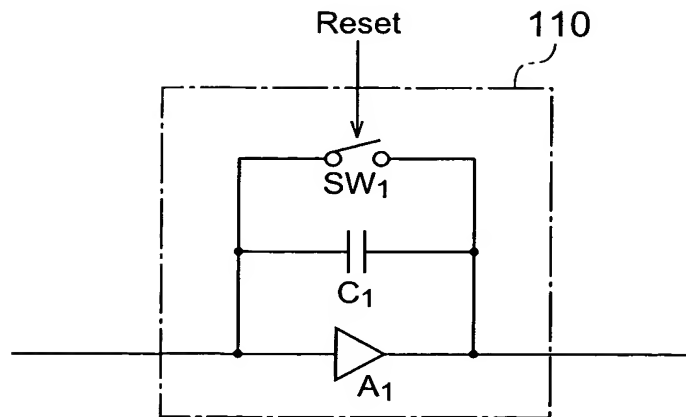
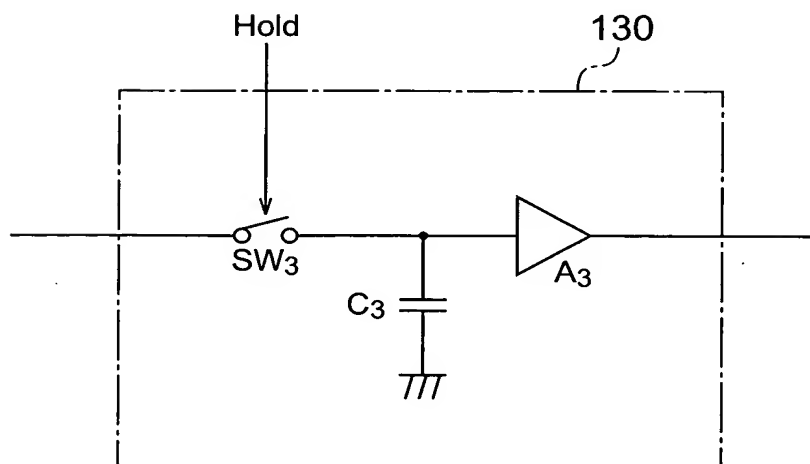


Fig. 10



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Fig.11**Fig.12**

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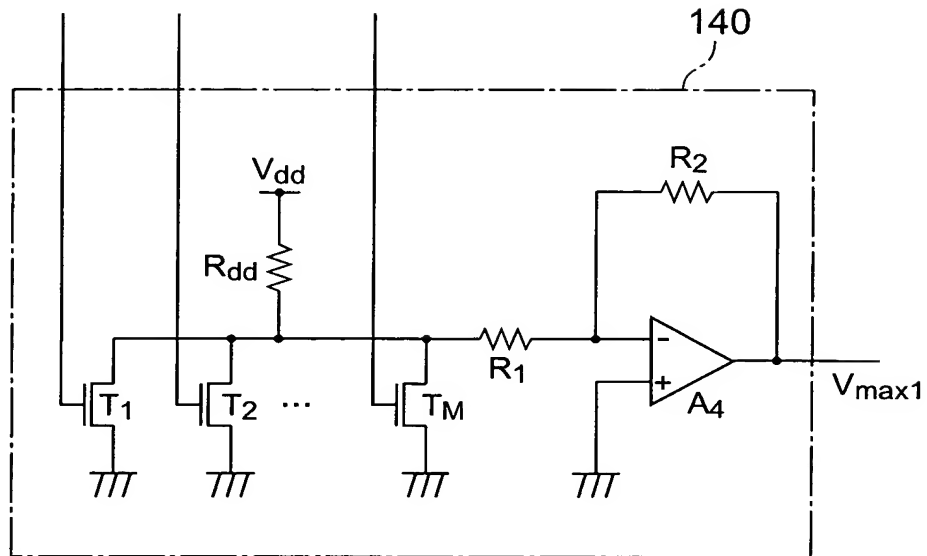
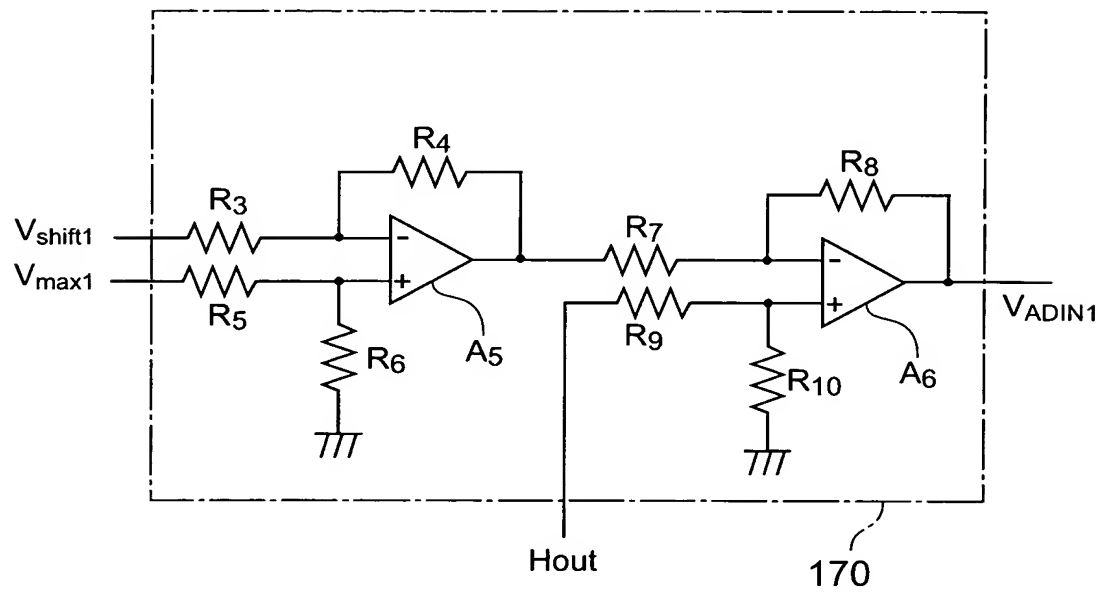
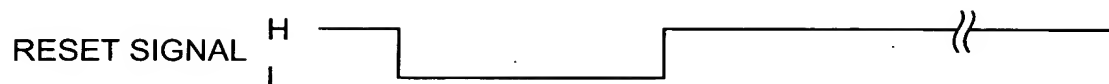
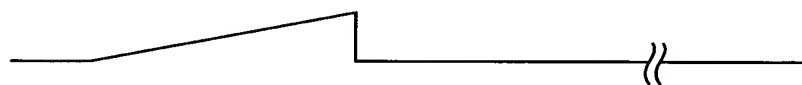
Fig.13**Fig.14**

Fig.15A**Fig.15B**

1ST INTEGRATING
CIRCUIT OUTPUT

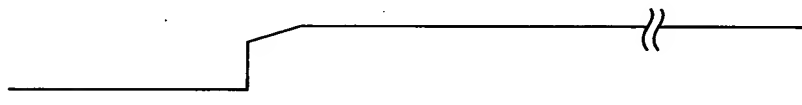
**Fig.15C**

HOLD SIGNAL

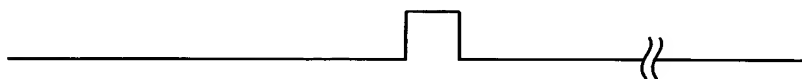
H
L

**Fig.15D**

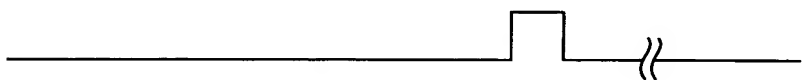
1ST S/H CIRCUIT
OUTPUT

**Fig.15E**

shift(H1)

**Fig.15F**

shift(H2)

**Fig.15G**

shift(Hm)

**Fig.15H**

Hout



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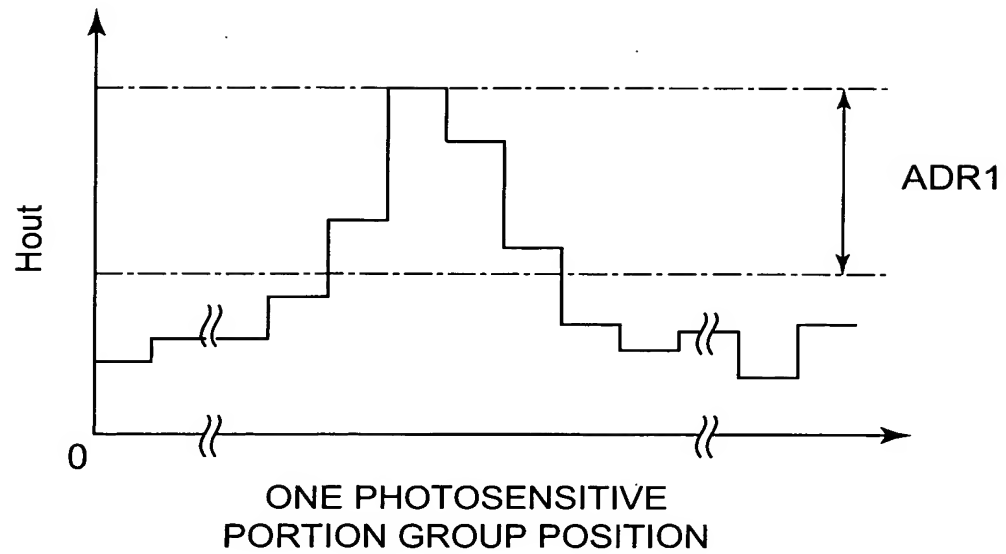
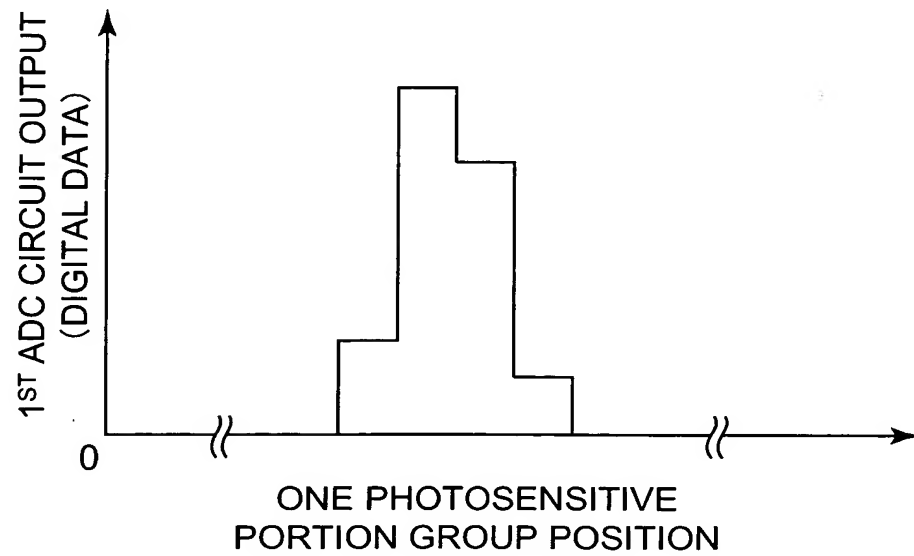
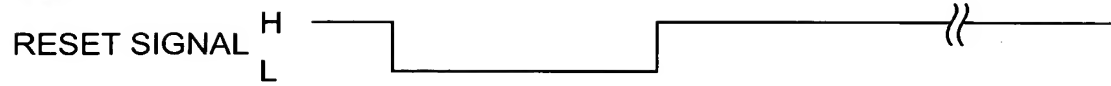
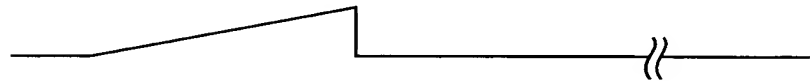
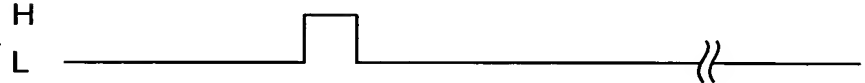
Fig.16**Fig.17**

Fig.18A**Fig.18B**

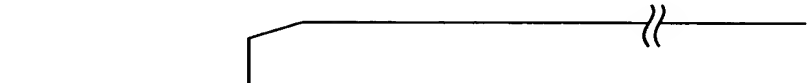
2ND INTEGRATING
CIRCUIT OUTPUT

**Fig.18C**

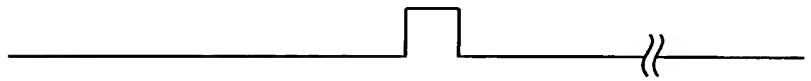
HOLD SIGNAL

**Fig.18D**

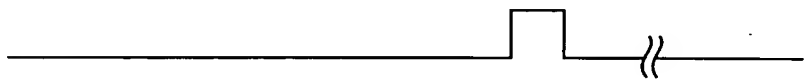
2ND S/H
CIRCUIT OUTPUT

**Fig.18E**

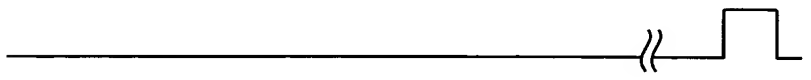
shift(V₁)

**Fig.18F**

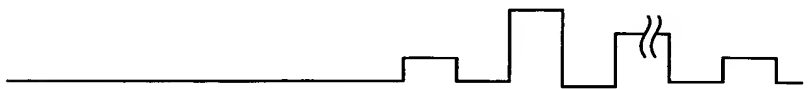
shift(V₂)

**Fig.18G**

shift(V_N)

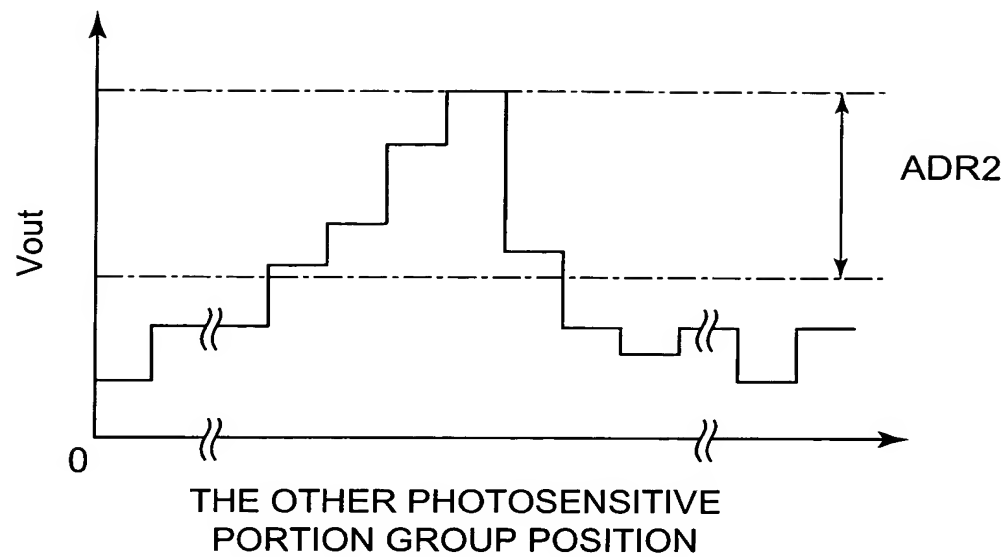
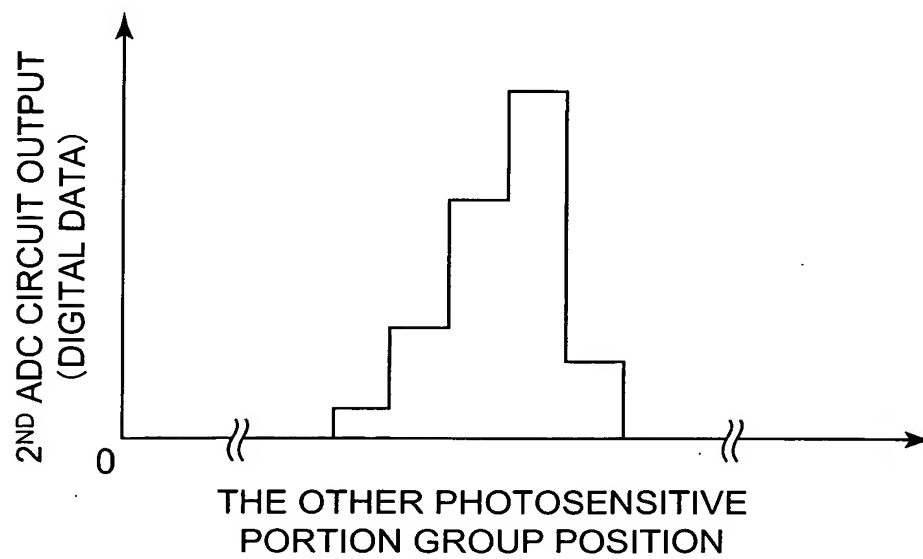
**Fig.18H**

V_{out}



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Fig.19**Fig.20**

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Fig. 21

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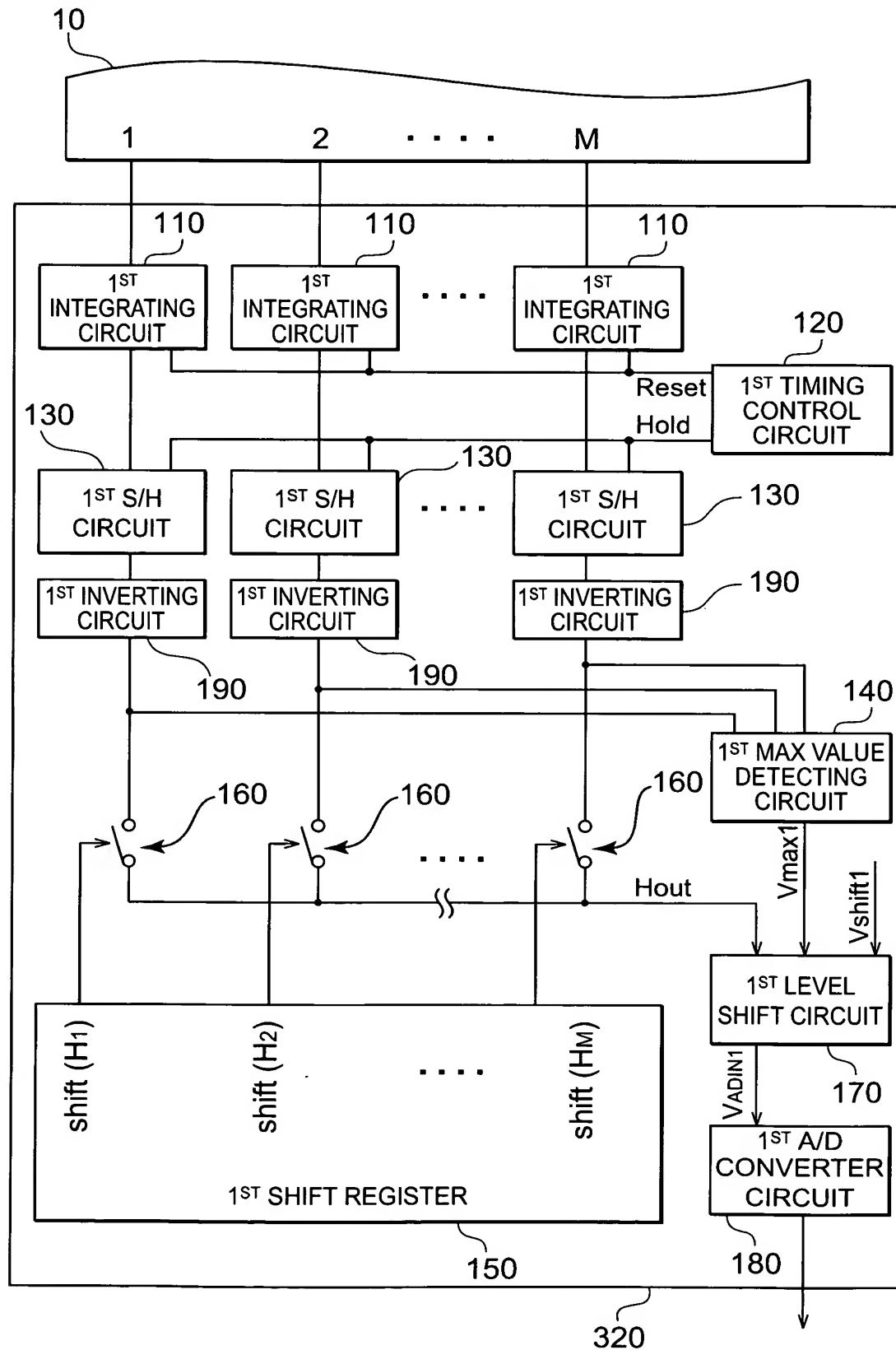


Fig. 22

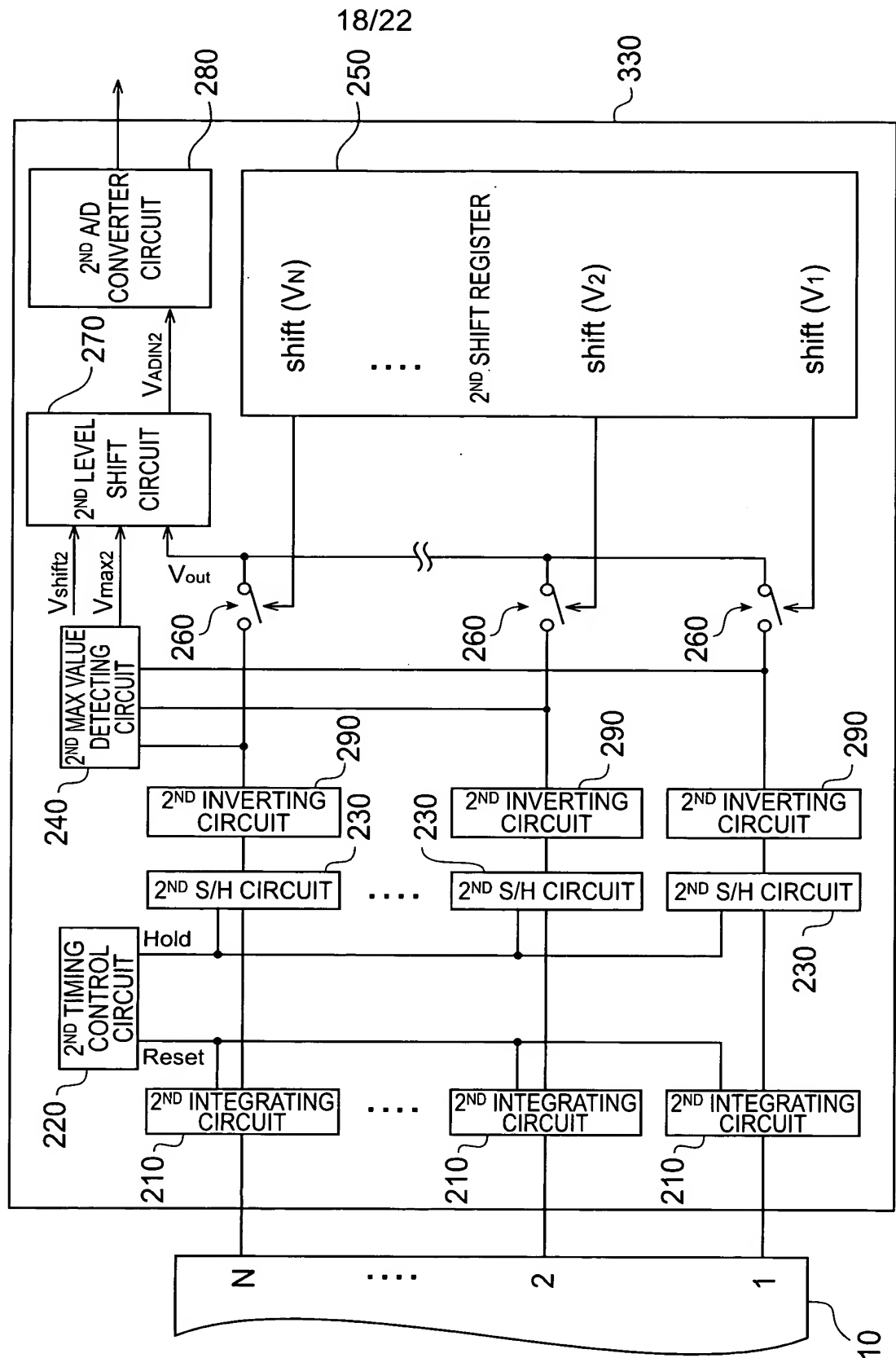
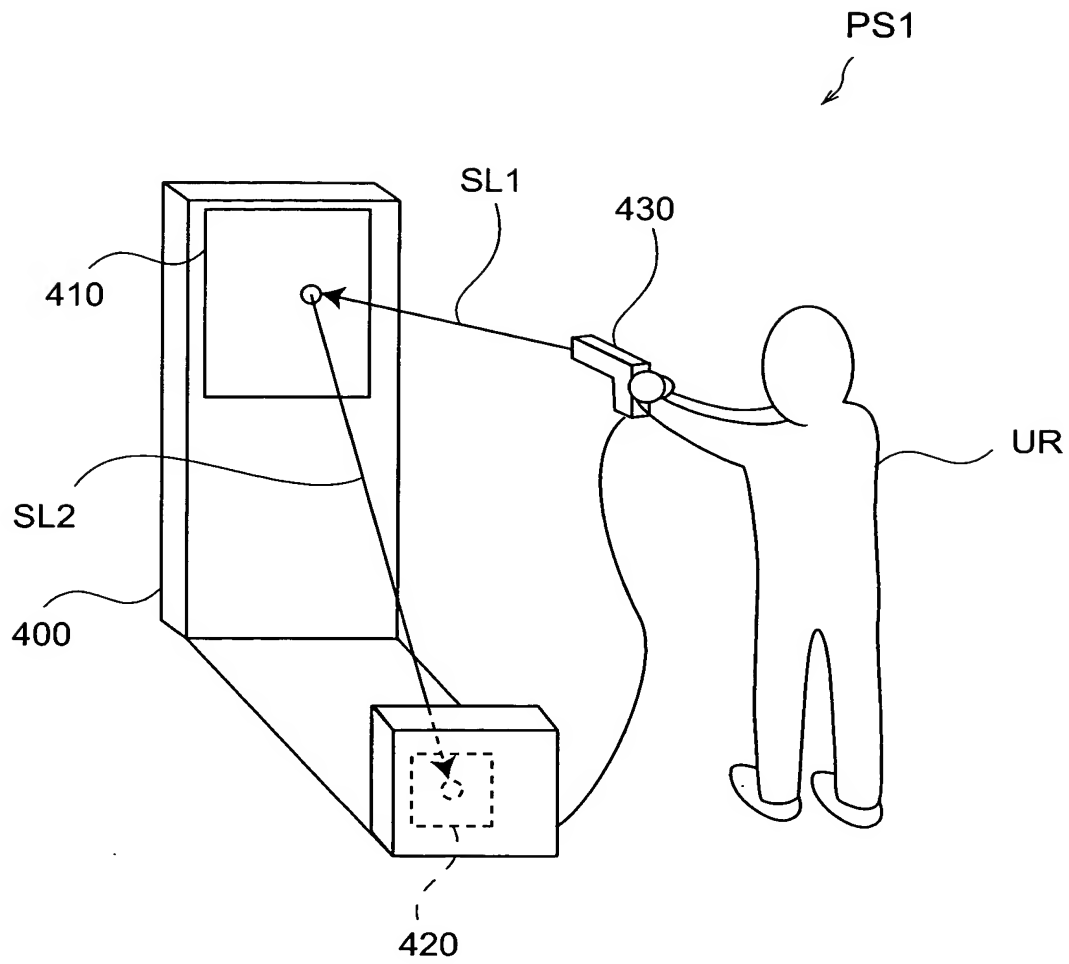


Fig.23

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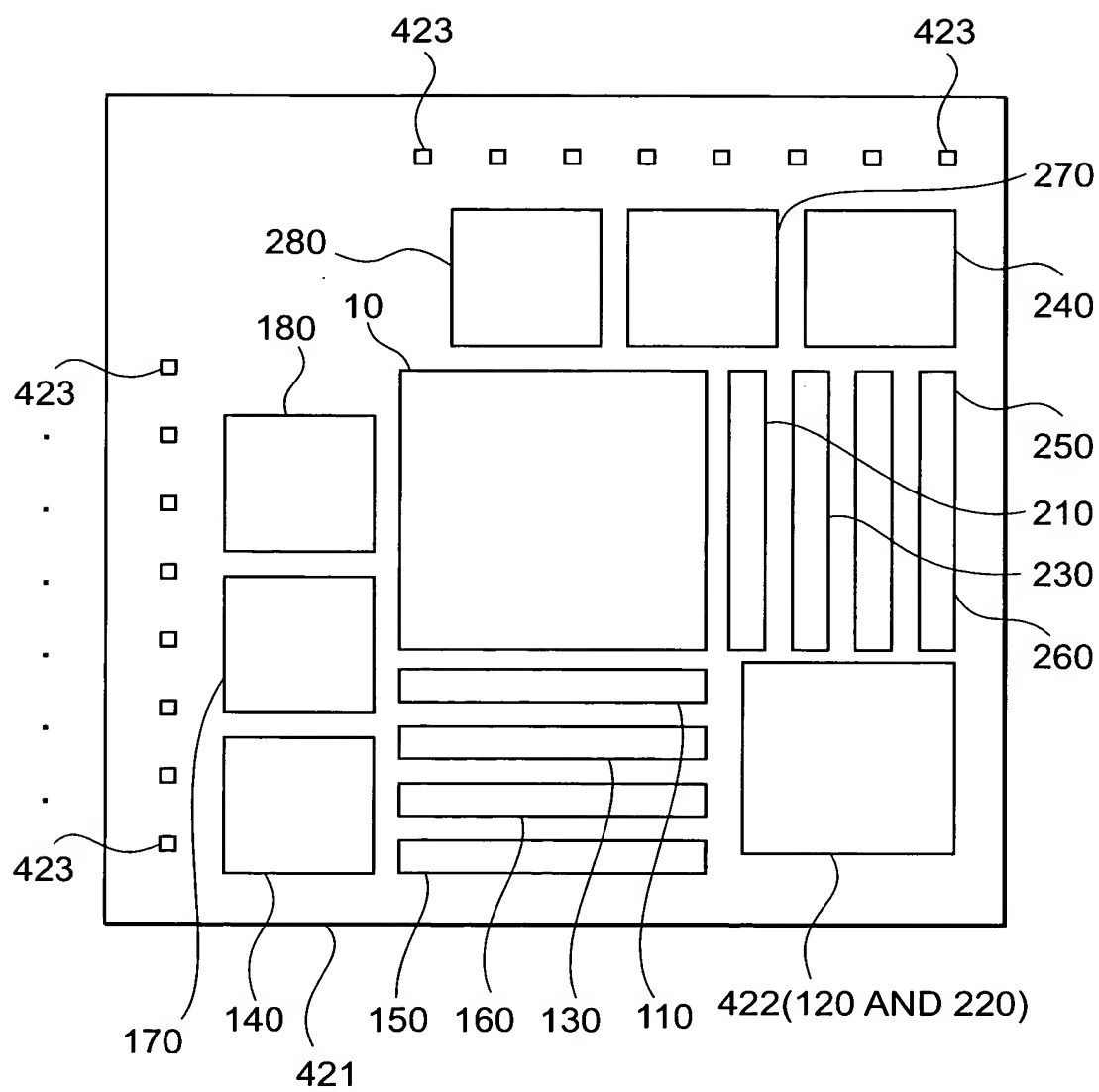
Fig.24

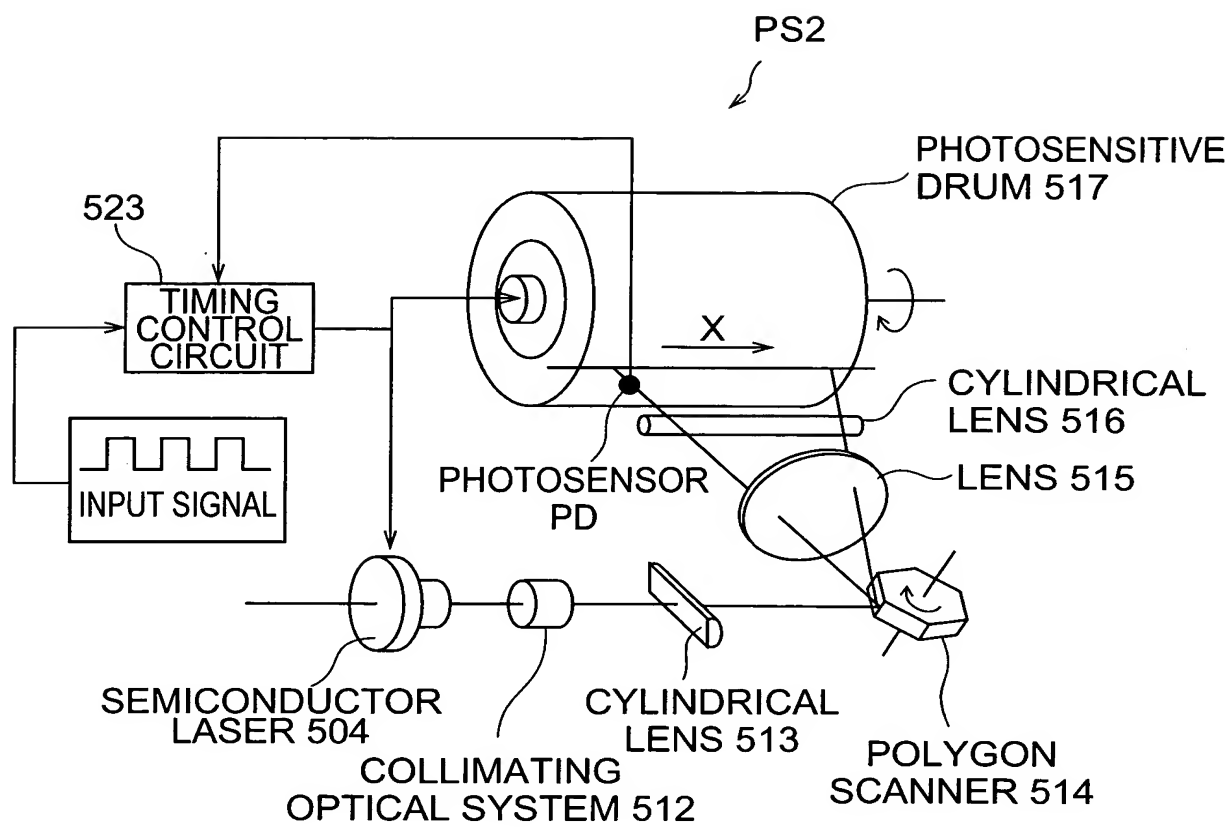
Fig.25

Fig. 26